

# LM776 Multi-Purpose Programmable Operational Amplifier

## General Description

The LM776 Programmable Operational Amplifier is constructed using the Planar Epitaxial process. High input impedance, low supply currents, and low input noise over a wide range of operating supply voltages coupled with programmable electrical characteristics result in an extremely versatile amplifier for use in high accuracy, low power consumption analog applications. Input noise voltage and current, power consumption, and input current can be optimized by a single resistor or current source that sets the chip quiescent current for nano watt power consumption or for characteristics similar to the LM741. Internal frequency compensation, absence of latch up, high slew rate and short circuit current protection assure ease of use in long time integrators, active filters, and sample and hold circuits.

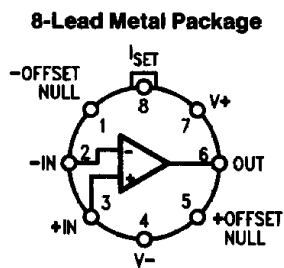
## Features

- Micropower consumption
- $\pm 1.2V$  to  $\pm 18V$  operation
- No frequency compensation required
- Low input bias currents
- Wide programming range
- High slew rate
- Low noise
- Short circuit protection
- Offset null capability
- No latch up

## Applications

- Battery-powered instrumentation
- High-gain amp
- Filters
- Sample-and-Hold

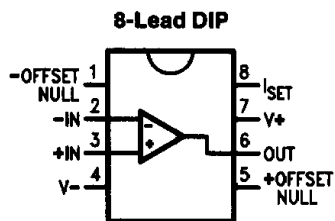
## Connection Diagrams



Top View

TL/H/10073-1

Lead 4 connected to case.



Top View

TL/H/10073-2

## Ordering Information

| Device Code | Package Code | Package Description |
|-------------|--------------|---------------------|
| LM776H      | H08A         | Metal               |
| LM776CH     | H08A         | Metal               |
| LM776CN     | N08E         | Molded DIP          |

## Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

|   |  |                 |
|---|--|-----------------|
| Storage Temperature Range               |  |                 |
| Metal Can                               |  | -65°C to +175°C |
| Molded DIP                              |  | -65°C to +150°C |
| Operating Temperature Range             |  |                 |
| Extended (LM776M)                       |  | -55°C to +125°C |
| Commercial (LM776C)                     |  | 0°C to +70°C    |
| Lead Temperature                        |  |                 |
| Metal Can (Soldering, 60 sec.)          |  | 300°C           |
| Molded DIP (Soldering, 10 sec.)         |  | 265°C           |
| Internal Power Dissipation (Notes 1, 2) |  |                 |
| 8L-Metal Can                            |  | 1.00W           |
| 8L-Molded DIP                           |  | 0.93W           |

|   |   |
|---|---|
| Supply Voltage  | ±18V  |
| Differential Input Voltage  | ±30V  |
| Input Voltage (Note 3)  | ±15V  |
| Voltage Between Offset Null and V-                                | ±0.5V   |
| Output Short Circuit Duration (Note 4)                            | Indefinite  |
| I <sub>SET</sub> (Maximum Current at I <sub>SET</sub> )           | 500 μA  |
| V <sub>SET</sub> (Maximum Voltage to Ground at I <sub>SET</sub> ) | (V <sup>+</sup> - 2.0V) ≤ V <sub>SET</sub> ≤ V <sup>+</sup> |
| ESD Tolerance   | (to be determined)  |

## LM776

### Electrical Characteristics T<sub>A</sub> = 25°C, V<sub>CC</sub> = ±15V, unless otherwise specified

| Symbol              | Parameter                             | Conditions                                     | I <sub>SET</sub> = 1.5 μA   |     |      | I <sub>SET</sub> = 15 μA |      |     | Units |
|---------------------|---------------------------------------|--|---|-----|------|--------------------------|------|-----|-------|
|                     |                                       |  | Min   | Typ | Max  | Min                      | Typ  | Max |       |
| V <sub>IO</sub>     | Input Offset Voltage                  | R <sub>S</sub> ≤ 10 kΩ                         |   | 2.0 | 5.0  |                          | 2.0  | 5.0 | mV    |
| V <sub>IO adj</sub> | Input Offset Voltage Adjustment Range |  |   | 9.0 |      |                          | 18   |     | mV    |
| I <sub>IO</sub>     | Input Offset Current                  |  |   | 0.7 | 3.0  |                          | 2.0  | 15  | nA    |
| I <sub>B</sub>      | Input Bias Current                    |  |   | 2.0 | 7.5  |                          | 15   | 50  | nA    |
| Z <sub>I</sub>      | Input Impedance                       |  |   | 50  |      |                          | 5.0  |     | MΩ    |
| I <sub>CC</sub>     | Supply Current                        |  |   | 20  | 25   |                          | 160  | 180 | μA    |
| P <sub>c</sub>      | Power Consumption                     |  |   |     | 0.75 |                          |      | 5.4 | mW    |
| I <sub>OS</sub>     | Output Short Circuit Current          |  |   | 3.0 |      |                          | 12   |     | mA    |
| A <sub>VS</sub>     | Large Signal Voltage Gain             | V <sub>O</sub> = ±10V, R <sub>L</sub> ≥ 75 kΩ  | 200   | 400 |      |                          |      |     | V/mV  |
|                     |                                       | V <sub>O</sub> = ±10V, R <sub>L</sub> ≥ 5.0 kΩ |   |     |      | 100                      | 400  |     |       |
| V <sub>OP</sub>     | Output Voltage Swing                  | R <sub>L</sub> = 75 kΩ                         | ±12   | ±14 |      |                          |      |     | V     |
|                     |                                       | R <sub>L</sub> = 5.0 kΩ                        |   |     |      | ±10                      | ±13  |     |       |
| TR                  | Transient Response                    | Rise Time                                      | V <sub>I</sub> = 20 mV, R <sub>L</sub> = 5.0 kΩ,<br>C <sub>L</sub> = 100 pF, A <sub>V</sub> = 1.0 | 1.6 |      |                          | 0.35 |     | μs    |
|                     |                                       | Overshoot                                      |   | 0   |      |                          | 10   |     | %     |
| SR                  | Slew Rate                             | R <sub>L</sub> = 5.0 kΩ, A <sub>V</sub> = 1.0  |   | 0.1 |      |                          | 0.8  |     | V/μs  |

The following specifications apply -55°C ≤ T<sub>A</sub> ≤ +125°C

|                 |                              |   |     |    |     |     |    |     |      |
|-----------------|------------------------------|---|-----|----|-----|-----|----|-----|------|
| V <sub>IO</sub> | Input Offset Voltage         | R <sub>S</sub> ≤ 10 kΩ                        |     |    | 6.0 |     |    | 6.0 | mV   |
| I <sub>IO</sub> | Input Offset Current         | T <sub>A</sub> = +125°C                       |     |    | 5.0 |     |    | 15  | nA   |
|                 |                              | T <sub>A</sub> = -55°C                        |     |    | 10  |     |    | 40  |      |
| I <sub>B</sub>  | Input Bias Current           | T <sub>A</sub> = +125°C                       |     |    | 7.5 |     |    | 50  | nA   |
|                 |                              | T <sub>A</sub> = -55°C                        |     |    | 20  |     |    | 120 |      |
| I <sub>CC</sub> | Supply Current               |   |     |    | 30  |     |    | 200 | μA   |
| P <sub>c</sub>  | Power Consumption            |   |     |    | 0.9 |     |    | 6.0 | mW   |
| CMR             | Common Mode Rejection        | R <sub>S</sub> ≤ 10 kΩ                        | 70  | 90 |     | 70  | 90 |     | dB   |
| V <sub>IR</sub> | Input Voltage Range          |   | ±10 |    |     | ±10 |    |     | V    |
| PSRR            | Power Supply Rejection Ratio | R <sub>S</sub> ≤ 10 kΩ                        |     | 25 | 150 |     | 25 | 150 | μV/V |
| A <sub>VS</sub> | Large Signal Voltage Gain    | V <sub>O</sub> = +10V, R <sub>L</sub> ≥ 75 kΩ | 100 |    |     | 75  |    |     | V/mV |
| V <sub>OP</sub> | Output Voltage Swing         | R <sub>L</sub> = 75 kΩ                        | ±10 |    |     | ±10 |    |     | V    |

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Electrical Characteristics  $T_A = 25^\circ\text{C}$ ,  $V_{CC} = \pm 3.0\text{V}$ , unless otherwise specified

| Symbol               | Parameter                             | Conditions   | $I_{SET} = 1.5 \mu\text{A}$   |      |     | $I_{SET} = 15 \mu\text{A}$ |      |     | Units            |
|----------------------|---------------------------------------|--|---|------|-----|----------------------------|------|-----|------------------|
|                      |                                       |  | Min   | Typ  | Max | Min                        | Typ  | Max |                  |
| $V_{IO}$             | Input Offset Voltage                  | $R_S \leq 10 \text{ k}\Omega$                            |   | 2.0  | 5.0 |                            | 2.0  | 5.0 | mV               |
| $V_{IO \text{ adj}}$ | Input Offset Voltage Adjustment Range |  |   | 9.0  |     |                            | 18   |     | mV               |
| $I_{IO}$             | Input Offset Current                  |  |   | 0.7  | 3.0 |                            | 2.0  | 15  | nA               |
| $I_{IB}$             | Input Bias Current                    |  |   | 2.0  | 7.5 |                            | 15   | 50  | nA               |
| $Z_I$                | Input Impedance                       |  |   | 50   |     |                            | 5.0  |     | M $\Omega$       |
| $I_{CC}$             | Supply Current                        |  |   | 13   | 20  |                            | 130  | 160 | $\mu\text{A}$    |
| $P_c$                | Power Consumption                     |  |   | 78   | 120 |                            | 780  | 960 | $\mu\text{W}$    |
| $I_{OS}$             | Output Short Circuit Current          |  |   | 3.0  |     |                            | 5.0  |     | mA               |
| $A_{VS}$             | Large Signal Voltage Gain             | $V_O = \pm 1.0\text{V}$ , $R_L \geq 75 \text{ k}\Omega$  | 50  | 200  |     |                            |      |     | V/mV             |
|                      |                                       | $V_O = \pm 1.0\text{V}$ , $R_L \geq 5.0 \text{ k}\Omega$ |   |      |     | 50                         | 200  |     |                  |
| TR                   | Transient Response                    | Rise Time  | $V_I = 20 \text{ mV}$ , $R_L = 5.0 \text{ k}\Omega$ ,<br>$C_L = 100 \text{ pF}$ , $A_V = 1.0$ |      | 3.0 |                            | 0.6  |     | $\mu\text{s}$    |
|                      |                                       | Overshoot  |   | 0    |     | 5                          |      | %   |                  |
| SR                   | Slew Rate                             | $R_L = 5.0 \text{ k}\Omega$ , $A_V = 1.0$                |   | 0.03 |     |                            | 0.35 |     | V/ $\mu\text{s}$ |

The following specifications apply  $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$

|          |                              |  |           |           |     |           |           |      |                 |
|----------|------------------------------|--|-----------|-----------|-----|-----------|-----------|------|-----------------|
| $V_{IO}$ | Input Offset Voltage         | $R_S \leq 10 \text{ k}\Omega$                            |           |           | 6.0 |           |           | 6.0  | mV              |
| $I_{IO}$ | Input Offset Current         | $T_A = +125^\circ\text{C}$                               |           |           | 5.0 |           |           | 15   | nA              |
|          |                              | $T_A = -55^\circ\text{C}$                                |           |           | 10  |           |           | 40   |                 |
| $I_{IB}$ | Input Bias Current           | $T_A = +125^\circ\text{C}$                               |           |           | 7.5 |           |           | 50   | nA              |
|          |                              | $T_A = -55^\circ\text{C}$                                |           |           | 20  |           |           | 120  |                 |
| $I_{CC}$ | Supply Current               |  |           |           | 25  |           |           | 180  | $\mu\text{A}$   |
| $P_c$    | Power Consumption            |  |           |           | 150 |           |           | 1080 | $\mu\text{W}$   |
| CMR      | Common Mode Rejection        | $R_S \leq 10 \text{ k}\Omega$                            | 70        | 86        |     | 70        | 86        |      | dB              |
| $V_{IR}$ | Input Voltage Range          |  | $\pm 1.0$ |           |     | $\pm 1.0$ |           |      | V               |
| PSRR     | Power Supply Rejection Ratio | $R_S \leq 10 \text{ k}\Omega$                            |           | 25        | 150 |           | 25        | 150  | $\mu\text{V/V}$ |
| $A_{VS}$ | Large Signal Voltage Gain    | $V_O = \pm 1.0\text{V}$ , $R_L \geq 75 \text{ k}\Omega$  | 25        |           |     |           |           |      | V/mV            |
|          |                              | $V_O = \pm 1.0\text{V}$ , $R_L \geq 5.0 \text{ k}\Omega$ |           |           |     | 25        |           |      |                 |
| $V_{OP}$ | Output Voltage Swing         | $R_L = 75 \text{ k}\Omega$                               | $\pm 2.0$ | $\pm 2.4$ |     |           |           |      | V               |
|          |                              | $R_L = 5.0 \text{ k}\Omega$                              |           |           |     | $\pm 1.9$ | $\pm 2.1$ |      |                 |

**Note 1:**  $T_{J \text{ Max}} = 150^\circ\text{C}$  for the Molded DIP, and  $175^\circ\text{C}$  for the Metal Can.

**Note 2:** Ratings apply to ambient temperature at  $25^\circ\text{C}$ . Above this temperature, derate the 8L-Metal Can at  $6.7 \text{ mW}/^\circ\text{C}$ , and the 8L-Molded DIP at  $7.5 \text{ mW}/^\circ\text{C}$ .

**Note 3:** For supply voltages less than  $\pm 15\text{V}$ , the absolute maximum input voltage is equal to the supply voltage.

**Note 4:** Short Circuit may be to ground or either supply. Rating applies to  $125^\circ\text{C}$  case temperature or  $75^\circ\text{C}$  ambient temperature for  $I_{SET} \leq 30 \mu\text{A}$ .

## LM776C

Electrical Characteristics  $T_A = 25^\circ\text{C}$ ,  $V_{CC} = \pm 15\text{V}$ , unless otherwise specified

| Symbol               | Parameter                             | Conditions  | $I_{SET} = 1.5 \mu\text{A}$  |          |     | $I_{SET} = 15 \mu\text{A}$ |          |     | Units            |
|----------------------|---------------------------------------|---|--|----------|-----|----------------------------|----------|-----|------------------|
|                      |                                       |   | Min  | Typ      | Max | Min                        | Typ      | Max |                  |
| $V_{IO}$             | Input Offset Voltage                  | $R_S \leq 10 \text{ k}\Omega$                           |  | 2.0      | 6.0 |                            | 2.0      | 6.0 | mV               |
| $V_{IO \text{ adj}}$ | Input Offset Voltage Adjustment Range |   |  | 9.0      |     |                            | 18       |     | mV               |
| $I_{IO}$             | Input Offset Current                  |   |  | 0.7      | 6.0 |                            | 2.0      | 25  | nA               |
| $I_{IB}$             | Input Bias Current                    |   |  | 2.0      | 10  |                            | 15       | 50  | nA               |
| $Z_I$                | Input Impedance                       |   |  | 50       |     |                            | 5.0      |     | M $\Omega$       |
| $I_{CC}$             | Supply Current                        |   |  | 20       | 30  |                            | 160      | 190 | $\mu\text{A}$    |
| $P_c$                | Power Consumption                     |   |  |          | 0.9 |                            |          | 5.7 | mW               |
| $I_{OS}$             | Output Short Circuit Current          |   |  | 3.0      |     |                            | 12       |     | mA               |
| $A_{VS}$             | Large Signal Voltage Gain             | $V_O = \pm 10\text{V}$ , $R_L \geq 75 \text{ k}\Omega$  | 50   | 400      |     |                            |          |     | V/mV             |
|                      |                                       | $V_O = \pm 10\text{V}$ , $R_L \geq 5.0 \text{ k}\Omega$ |  |          |     | 50                         | 400      |     |                  |
| $V_{OP}$             | Output Voltage Swing                  | $R_L = 75 \text{ k}\Omega$                              | $\pm 12$   | $\pm 14$ |     |                            |          |     | V                |
|                      |                                       | $R_L = 5.0 \text{ k}\Omega$                             |  |          |     | $\pm 10$                   | $\pm 13$ |     |                  |
| TR                   | Transient Response                    | Rise Time   | $V_I = 20 \text{ mV}$ , $R_L \geq 5.0 \text{ k}\Omega$ ,<br>$C_L = 100 \text{ pF}$ , $A_V = 1.0$ | 1.6      |     |                            | 0.35     |     | $\mu\text{s}$    |
|                      |                                       | Overshoot   |  | 0        |     |                            | 10       |     | %                |
| SR                   | Slew Rate                             | $R_L = 5.0 \text{ k}\Omega$ , $A_V = 1.0$               |  | 0.1      |     |                            | 0.8      |     | V/ $\mu\text{s}$ |

The following specifications apply  $0^\circ\text{C} \leq T_A \leq +70^\circ\text{C}$

|          |                              |  |          |    |      |  |          |     |                        |
|----------|------------------------------|--|----------|----|------|--|----------|-----|------------------------|
| $V_{IO}$ | Input Offset Voltage         | $R_S \leq 10 \text{ k}\Omega$                          |          |    | 7.5  |  |          | 7.5 | mV                     |
| $I_{IO}$ | Input Offset Current         | $T_A = 70^\circ\text{C}$                               |          |    | 6.0  |  |          | 25  | nA                     |
|          |                              | $T_A = 0^\circ\text{C}$                                |          |    | 10   |  |          | 40  |                        |
| $I_{IB}$ | Input Bias Current           | $T_A = 70^\circ\text{C}$                               |          |    | 10   |  |          | 50  | nA                     |
|          |                              | $T_A = 0^\circ\text{C}$                                |          |    | 20   |  |          | 100 |                        |
| $I_{CC}$ | Supply Current               |  |          |    | 35   |  |          | 200 | $\mu\text{A}$          |
| $P_c$    | Power Consumption            |  |          |    | 1.05 |  |          | 6.0 | mW                     |
| CMR      | Common Mode Rejection        | $R_S \leq 10 \text{ k}\Omega$                          | 70       | 90 |      |  | 70       | 90  | dB                     |
| $V_{IR}$ | Input Voltage Range          |  | $\pm 10$ |    |      |  | $\pm 10$ |     | V                      |
| PSRR     | Power Supply Rejection Ratio | $R_S \leq 10 \text{ k}\Omega$                          |          | 25 | 200  |  | 25       | 200 | $\mu\text{V}/\text{V}$ |
| $A_{VS}$ | Large Signal Voltage Gain    | $V_O = \pm 10\text{V}$ , $R_L \geq 75 \text{ k}\Omega$ | 50       |    |      |  | 50       |     | V/mV                   |
| $V_{OP}$ | Output Voltage Swing         | $R_L = 75 \text{ k}\Omega$                             | $\pm 10$ |    |      |  | $\pm 10$ |     | V                      |

## LM776C

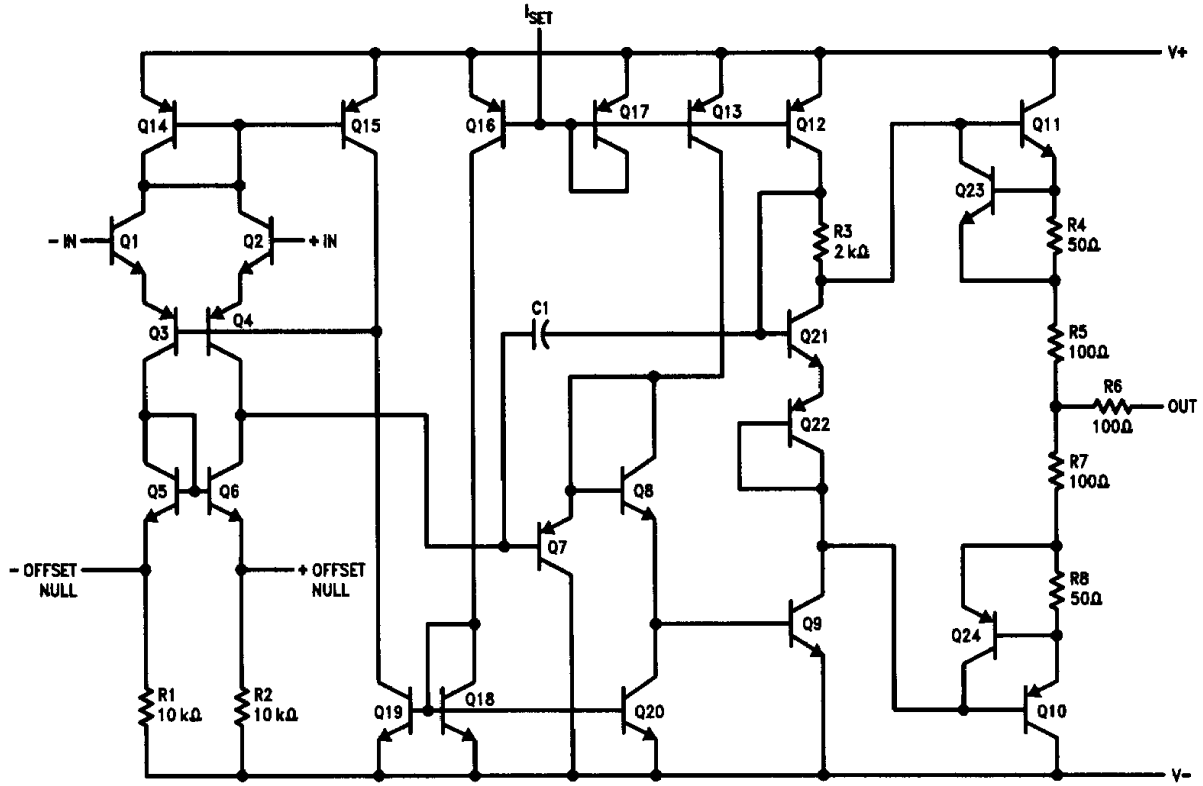
Electrical Characteristics  $T_A = 25^\circ\text{C}$ ,  $V_{CC} = \pm 3.0\text{V}$ , unless otherwise specified

| Symbol               | Parameter                             | Conditions   | $I_{SET} = 1.5 \mu\text{A}$  |      |     | $I_{SET} = 15 \mu\text{A}$ |      |      | Units            |
|----------------------|---------------------------------------|--|--|------|-----|----------------------------|------|------|------------------|
|                      |                                       |  | Min  | Typ  | Max | Min                        | Typ  | Max  |                  |
| $V_{IO}$             | Input Offset Voltage                  | $R_S \leq 10 \text{ k}\Omega$                            |  | 2.0  | 6.0 |                            | 2.0  | 6.0  | mV               |
| $V_{IO \text{ adj}}$ | Input Offset Voltage Adjustment Range |  |  | 9.0  |     |                            | 18   |      | mV               |
| $I_{IO}$             | Input Offset Current                  |  |  | 0.7  | 6.0 |                            | 2.0  | 25   | nA               |
| $I_{IB}$             | Input Bias Current                    |  |  | 2.0  | 10  |                            | 15   | 50   | nA               |
| $Z_I$                | Input Impedance                       |  |  | 50   |     |                            | 5.0  |      | M $\Omega$       |
| $I_{CC}$             | Supply Current                        |  |  | 13   | 20  |                            | 130  | 170  | $\mu\text{A}$    |
| $P_c$                | Power Consumption                     |  |  | 78   | 120 |                            | 780  | 1020 | $\mu\text{W}$    |
| $I_{OS}$             | Output Short Circuit Current          |  |  | 3.0  |     |                            | 5.0  |      | mA               |
| $A_{VS}$             | Large Signal Voltage Gain             | $V_O = \pm 1.0\text{V}$ , $R_L \geq 75 \text{ k}\Omega$  | 25   | 200  |     |                            |      |      | V/mV             |
|                      |                                       | $V_O = \pm 1.0\text{V}$ , $R_L \geq 5.0 \text{ k}\Omega$ |  |      |     | 25                         | 200  |      |                  |
| TR                   | Transient Response                    | Rise Time<br>Overshoot                                   | $V_I = 20 \text{ mV}$ , $R_L \geq 5.0 \text{ k}\Omega$ ,<br>$C_L = 100 \text{ pF}$ , $A_V = 1.0$ |      | 3.0 |                            |      | 0.6  | $\mu\text{s}$    |
|                      |                                       |  |  |      | 0   |                            |      | 5    | %                |
| SR                   | Slew Rate                             | $R_L = 5.0 \text{ k}\Omega$ , $A_V = 1.0$                |  | 0.03 |     |                            | 0.35 |      | V/ $\mu\text{s}$ |

The following specifications apply  $0^\circ\text{C} \leq T_A \leq +70^\circ\text{C}$

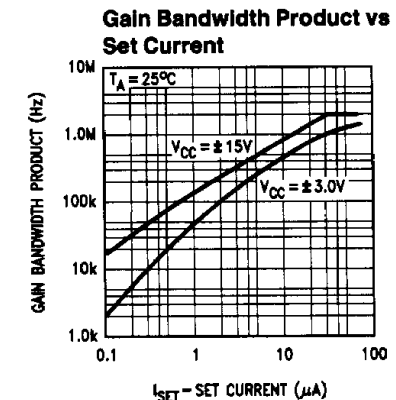
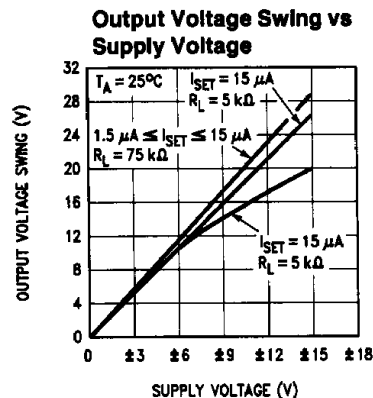
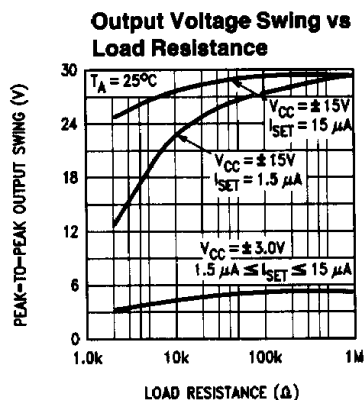
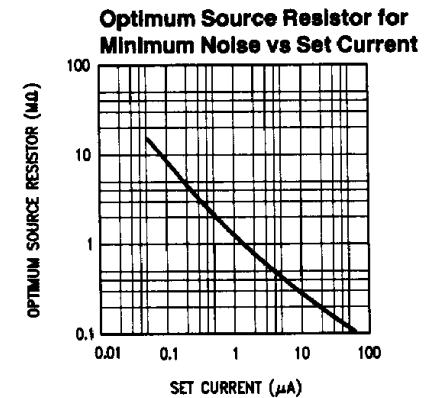
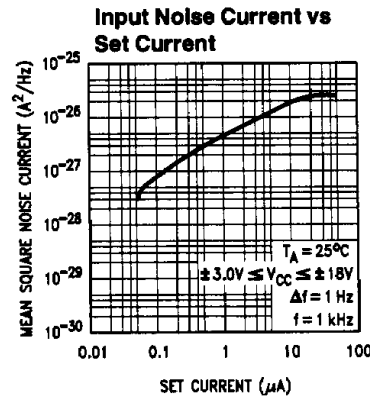
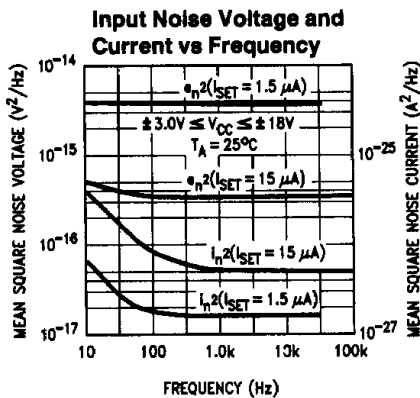
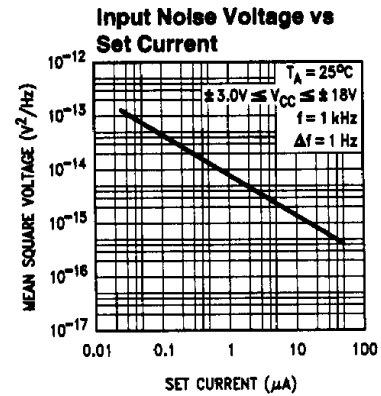
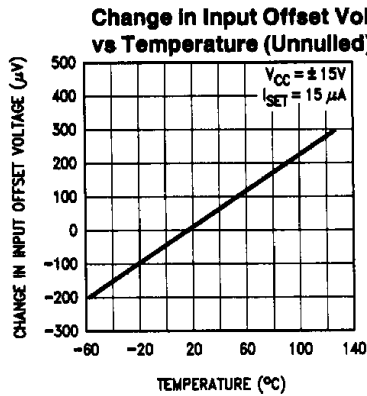
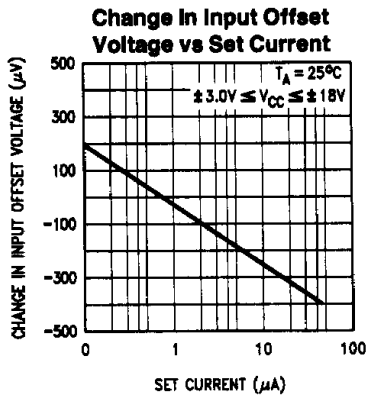
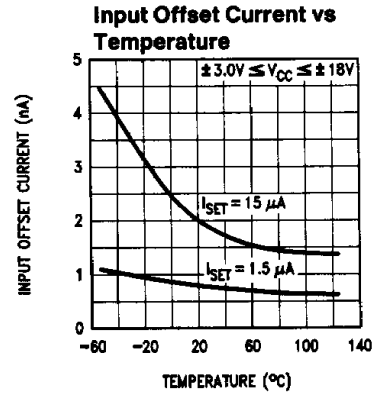
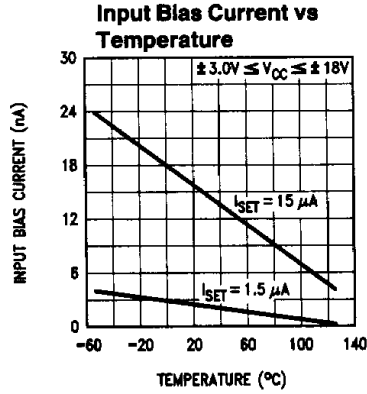
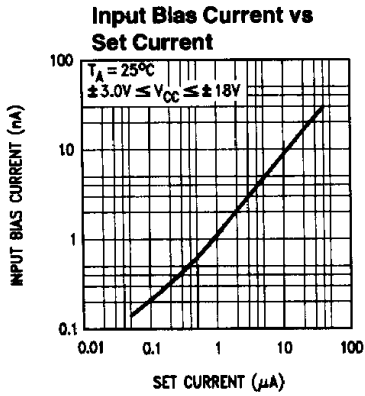
|          |                              |  |           |           |     |           |           |      |                 |
|----------|------------------------------|--|-----------|-----------|-----|-----------|-----------|------|-----------------|
| $V_{IO}$ | Input Offset Voltage         | $R_S \leq 10 \text{ k}\Omega$                            |           |           | 7.5 |           |           | 7.5  | mV              |
| $I_{IO}$ | Input Offset Current         | $T_A = 70^\circ\text{C}$                                 |           |           | 6.0 |           |           | 25   | nA              |
|          |                              | $T_A = 0^\circ\text{C}$                                  |           |           | 10  |           |           | 40   |                 |
| $I_{IB}$ | Input Bias Current           | $T_A = 70^\circ\text{C}$                                 |           |           | 10  |           |           | 50   | nA              |
|          |                              | $T_A = 0^\circ\text{C}$                                  |           |           | 20  |           |           | 100  |                 |
| $I_{CC}$ | Supply Current               |  |           |           | 25  |           |           | 180  | $\mu\text{A}$   |
| $P_c$    | Power Consumption            |  |           |           | 150 |           |           | 1080 | $\mu\text{W}$   |
| CMR      | Common Mode Rejection        | $R_S \leq 10 \text{ k}\Omega$                            | 70        | 86        |     | 70        | 86        |      | dB              |
| $V_{IR}$ | Input Voltage Range          |  | $\pm 1.0$ |           |     | $\pm 1.0$ |           |      | V               |
| PSRR     | Power Supply Rejection Ratio | $R_S \leq 10 \text{ k}\Omega$                            |           | 25        | 200 |           | 25        | 200  | $\mu\text{V/V}$ |
| $A_{VS}$ | Large Signal Voltage Gain    | $V_O = \pm 1.0\text{V}$ , $R_L \geq 75 \text{ k}\Omega$  | 25        |           |     |           |           |      | V/mV            |
|          |                              | $V_O = \pm 1.0\text{V}$ , $R_L \geq 5.0 \text{ k}\Omega$ |           |           |     | 25        |           |      |                 |
| $V_{OP}$ | Output Voltage Swing         | $R_L = 75 \text{ k}\Omega$                               | $\pm 2.0$ | $\pm 2.4$ |     |           |           |      | V               |
|          |                              | $R_L = 5.0 \text{ k}\Omega$                              |           |           |     | $\pm 2.0$ | $\pm 2.1$ |      |                 |

# Equivalent Circuit

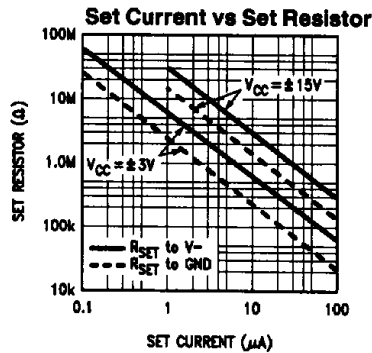
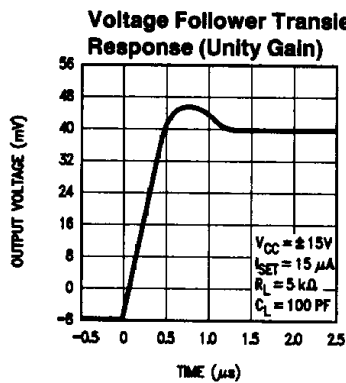
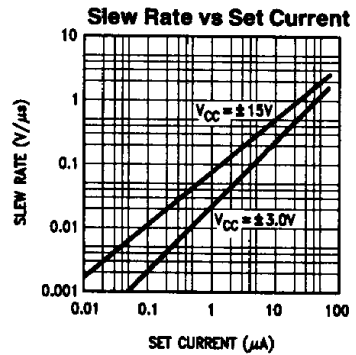
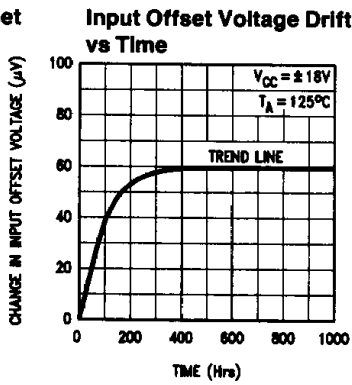
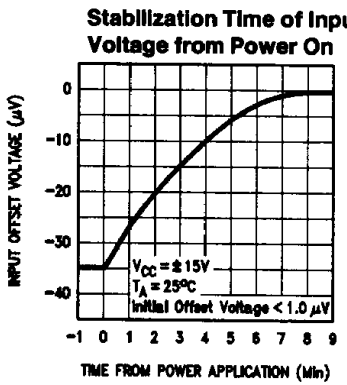
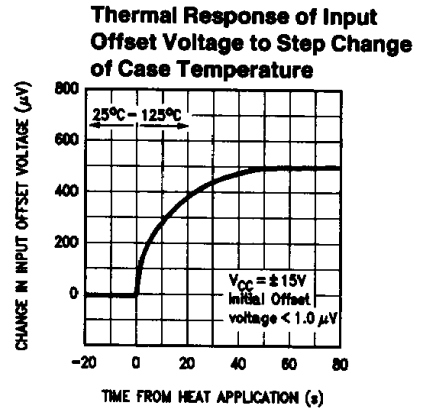
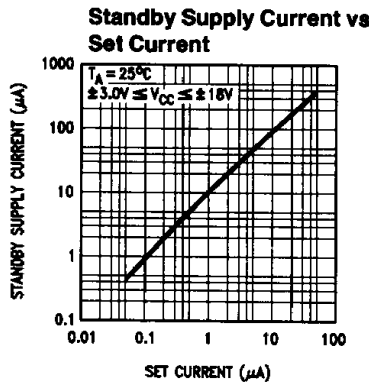
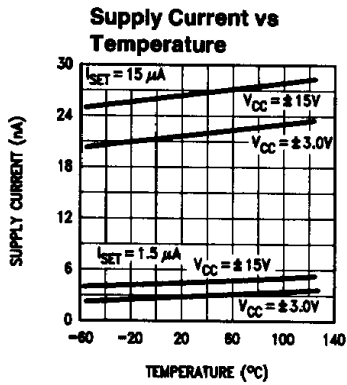
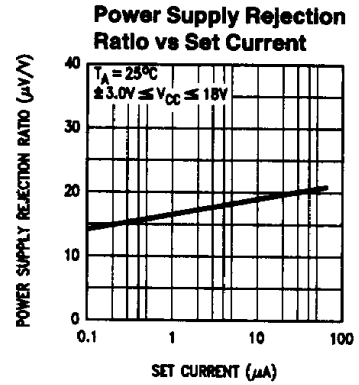
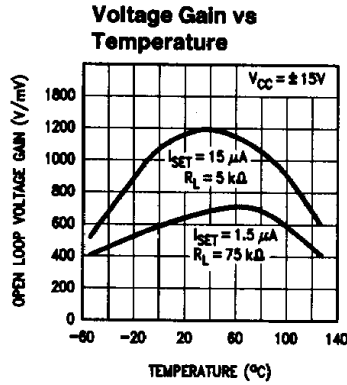
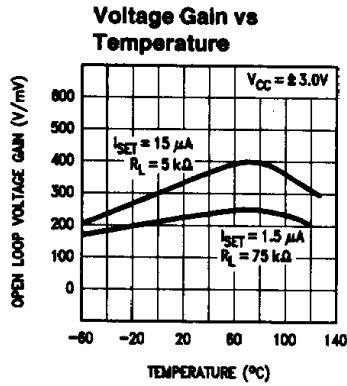


TL/H/10073-3

# Typical Performance Characteristics for LM776 and LM776C



# Typical Performance Characteristics for LM776 and LM776C (Continued)





# Applications Information

## Quiescent Current Setting Resistor (I<sub>SET</sub> to V<sup>-</sup>)

| V <sub>S</sub> | I <sub>SET</sub> |        |
|----------------|------------------|--------|
|                | 1.5 μA           | 15 μA  |
| ±1.5V          | 1.7 MΩ           | 170 kΩ |
| ±3.0V          | 3.6 MΩ           | 360 kΩ |
| ±6.0V          | 7.5 MΩ           | 750 kΩ |
| ±15V           | 20 MΩ            | 2.0 MΩ |

Note: The LM776 may be operated with R<sub>SET</sub> connected to ground or V<sup>-</sup>.

## I<sub>SET</sub> Equations

$$I_{SET} = \frac{(V^+) - 0.7 - (V^-)}{R_{SET}}$$

where:

R<sub>SET</sub> is connected to V<sup>-</sup>

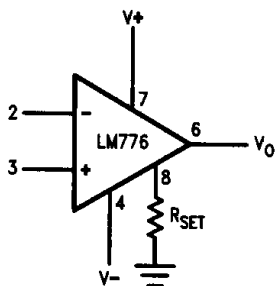
$$I_{SET} = \frac{(V^+) - 0.7}{R_{SET}}$$

where:

R<sub>SET</sub> is connected to ground.

## Biasing Circuits

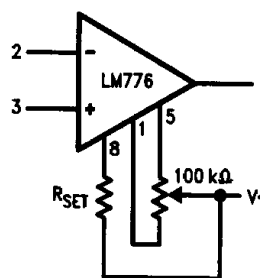
### Resistor Biasing



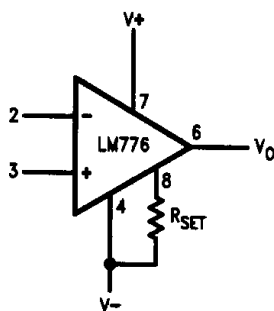
TL/H/10073-6

R<sub>SET</sub> Connected to Ground

### Voltage Offset Null Circuit



TL/H/10073-9

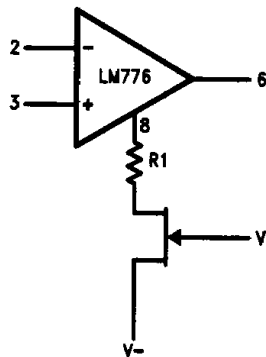


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R<sub>SET</sub> Connected to V<sup>-</sup>

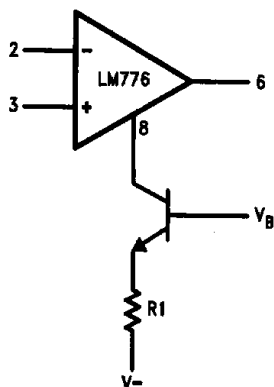
\*Recommended for supply voltages less than ±6V.

### FET Current Source Biasing



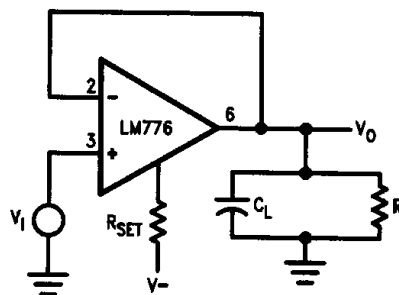
TL/H/10073-10

### Transistor Current Biasing



TL/H/10073-8

### Transient Response Test Circuit



TL/H/10073-11